

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Beilin et al.  
Serial No.: Unassigned  
Filed: Herewith  
For: METHODS OF PLANARIZING  
STRUCTURES ON WAFERS  
AND SUBSTRATES BY POLISHING  
Examiner: S. Ahmed  
Group Art Unit: 1746  
Attorney Docket: 25916-111 (53755)

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**June 12, 2001**

By

  
Jordan Wilson

JC971 U.S. PTO  
09/881514  
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P.7.

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**INFORMATION DISCLOSURE STATEMENT  
PURSUANT TO 37 C.F.R. §1.56 AND §§1.97-1.98**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

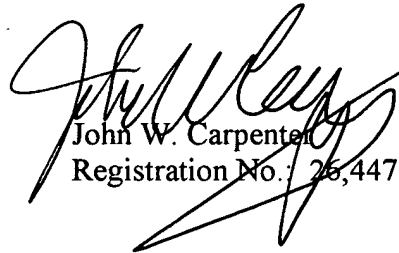
The citations listed on the enclosed PTO-1449 Form are submitted under 37 C.F.R. §§1.97 and 1.98, and in compliance with the duty of disclosure as defined in 37 C.F.R. §1.56. The prior art references cited were made of record in prior U.S. application serial no 09/259,125 and copies of these references can be found in the respective file. The Examiner is requested to make these citations officially of record in the application filed herewith. This Information Disclosure Statement is being submitted before receipt of the first Office Action for the above-identified application. Consequently, pursuant to 37 C.F.R. §1.97, no fee or certification is required.

## INFORMATION DISCLOSURE STATEMENT

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This Information Disclosure Statement Under 37 C.F.R. §1.56 is not to be construed as a representation that any of the listed citations establishes, by itself or in combination with other information, a *prima facie* case of unpatentability of any claim in the above-identified application. Additionally, this Information Disclosure Statement is not to be construed as a representation that a further search of the art has been made by Applicants, or that additional information relevant to the examination of this application does not exist unbeknownst to Applicants.

Respectfully submitted,



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June 12, 2001  
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